

EAST Search History

EAST Search History (Prior Art)

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|---------|---|---|------------------|---------|---------------------|
| L1 | 0 | computer adj on adj a adj chip | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:30 |
| L2 | 250691 | (single one) near3 chip near "3" (microcomputer micro adj computer) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:32 |
| L3 | 9834 | (single one) near3 chip near3 (microcomputer micro adj computer) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:32 |
| L4 | 116262 | memory adj controller | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:32 |
| L5 | 637 | 3 and 4 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:33 |
| L6 | 45883 | intel.as. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:33 |
| L7 | 5 | 5 and 6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:33 |
| L8 | 24979 | (multichip multi adj chip) near3 module | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:38 |
| L9 | 1943921 | microprocessor micro adj processor processor ASIC DSP | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:39 |

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|-----|--------|------------------------------|---|----|----|---------------------|
| L10 | 156335 | memory near3 (die chip IC) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:40 |
| L11 | 94906 | 9 near3 (die chip IC) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:40 |
| L12 | 5231 | 4 and 10 and 11 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:41 |
| L13 | 320 | 8 and 12 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2010/07/07 23:41 |

EAST Search History (Interference)

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7/ 7/ 10 11:57:13 PM**C:\ Documents and Settings\ tnguyen31\ My Documents\ EAST\ Workspaces\ thinhnew2.wsp**